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Answer: A linear integrated circuit is a solid state analog device characterized by a theoretically infinite number of possible operating states. It operates over a continuous range of input levels. Within a certain input range, the

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channel length of a MOSFET in a saturation decreases with increase in A. gate voltage B.drain voltage

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*Response times vary by subject and question complexity. Median response time is 34 minutes and may be longer for new subjects. Q: Analog data having highest harmonic at 30 kHz generated

by a sensor has been digitized using 6 level... A: Given data: The highest harmonic (frequency) present in the ...

Answered: Discuss parallel tuned circuit with... | bartleby

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consumed in resistance and is equal to $I^2 R$ (b) exchange of power takes place between inductor and supply line (c) exchange of power takes place between capacitor and supply line (d) exchange of power does not take place between resistance and the supply line

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3. The main criterion in the design of a digital circuit is the reduction of. A) volume B) cost C) weight D) size. 4. Logic circuits are. A) analog circuits B) hybrid circuits C) none of the answers D) digital circuits. 5. The NAND-NAND realization is equivalent to. A) AND-NOT realization B) NOT-OR realization C) OR-AND realization D) AND-OR ...