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Bolsens, I.; Hugo J. de Man et al:
Hardware/Software Co-Design of Digital Telecommunication Systems. Proceedings of the IEEE, Vol. 85, No. 3, March 1997 Google Scholar [7.5]

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hardware/software co-
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datapath and data-
aware dynamic
scheduling. Aware of
data dependencies

extracted from the
decoupled datapath,
GraphDynS can
elaborately schedule
the program on-the-fly
to maximize
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system level objectives
by exploiting the
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Proceedings of the
26th 2014 International
Symposium on
Implementation and

Application of Functional Languages. ACM. 2014, p. 8 Paper B Markus Aronsson and Mary Sheeran. \Hardware Software Co-Design in Haskell". Proceedings of the 10th ACM SIGPLAN International Symposium on Haskell. ACM. 2017, pp. 162{173

A Functional Approach to

Hardware Software Co-Design.

Aspects of systems architecture in hardware/software co-design. In E. Lee, G. de Micheli, W. Rosenstiel, & L. Thiele (Eds.), Design Automation for Embedded Systems (pp. 22). Schloss Dagstuhl - Leibniz Zentrum für Informatik. Dagstuhl Seminar Proceedings, Vol.. 143