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Procedural steps of an example

instruction (MOV C, A) (Hexcode: 4F) being fetched & executed by microprocessor from memory location 2005: Step 1: 8085 places 16-bits address (2005H) from program counter on address bus Step 2: Control unit activates RD signal to enable memory chip (RAM) Step 3: Instruction byte (4F) is placed by memory on data bus Step 4: Instruction is decoded by Instruction decoder in binary form which directs timing & control unit to generate appropriate control signals to ...

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The 8085/8080A has six general-purpose registers to store 8-bit data; these are identified as B,C,D,E,H, and L as shown in the figure. They can be combined as register pairs - BC, DE, and HL - to

perform some 16-bit operations. The programmer can use these registers to store or copy data into the registers by using data copy instructions.

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